Application No.	Applicant(s)	
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Examiner	Art Unit	
Toniae M. Thomas	2822	
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6. ☐ Interview Sun Paper No./M 98), , 7. ⊠ Examiner's Al	nmary (PTO-413), ail Date mendment/Comment	
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EXAMINER'S AMENDMENT

Amendment

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

The application has been amended as follows:

In Specification

Paragraph [01] has been replaced with the following paragraph:

--This application is a continuation of U.S. Patent Application Serial No. 09/249,388, filed on February 12, 1999, now Patent No. US 6,258,660 B1, which is a divisional of U.S. Patent Application Serial No. 8/940,307, filed on September 30, 1997, now US 6,476,435, both of which are incorporated herein by reference. –

Reasons for Allowance

2. The following is an examiner's statement of reasons for allowance: the prior art of record does not anticipate, teach or suggest a process of forming a container cell substantially as claimed. For example, the Lee et al. patent (Lee – US 5,343,354) discloses a process of forming a container cell, wherein the process comprises the steps of: etching a container cell into an isolation film within a trench, the container cell being situated substantially between a first

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gate stack and a second gate stack and having an edge defined by a semiconductor substrate and the isolation film, the edge of the container cell substantially extending to and terminating at each of the first and second gate stacks; forming a storage node within the container cell; forming a cell dielectric over the storage node; and forming a cell plate over the cell dielectric. However, the prior art of record does not anticipate, teach or suggest, either separately or combined, a process of forming a container cell substantially as claimed, wherein the process comprises: etching a container cell into an isolation film within a trench, the container cell being situated substantially between a first gate stack and a second gate stack and having an edge defined by a semiconductor substrate and the isolation film, the edge of the container cell substantially extending to and terminating at each of the first and second gate stacks; forming a storage node within the container cell; forming a cell dielectric over the storage node; forming a cell plate over the cell dielectric, and forming an insulation layer over the cell plate and within the container cell.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toniae M. Thomas whose telephone number is (571) 272-1846. The examiner can normally be reached on Monday-Thursday from 8:30 a.m. to 5:30 p.m..

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on (571) 272-1852. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TMT

25 June 2004

Mary Wilczewski Primary Examiner

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